AF/2827

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

SARY P. MORRISON ET AL.

Serial No. 10/034,827 (TI-31373)

Filed December 26, 2001 (As noted in Petition)

For: CHIP-SCALE PACKAGES STACKED ON FOLDED INTERCONNECTOR FOR VERTICAL ASSEMBLY ON SUBSTRATES

Art Unit 2827

Examiner James M. Mitchell

Customer No. 23494

Director of the United States Patent and Trademark Office P. O. Box 1450 Alexandria, VA 22313-1450 CERTIFICATE OF MAILING OR TRANSMISSION UNDER 37 CFR 1.8

I hereby certify that the attached document is being deposited with the United States Postal Service with sufficient postage for First Class Mail in an envelope addressed to Director of the United States Patent and Trademark Office, P.O. Box 1450,, Alexandria, VA 22313-1450 or is being facsimile transmitted on the date indicated below:

F-7-09

Jav M. Cantor, Reg. No. 19,906

AMENDMENT UNDER 37 CFR 1.16

Sir:

Responsive to the Office action dated June 16, 2004, please amend the above identified application as follows: